

Title

Size

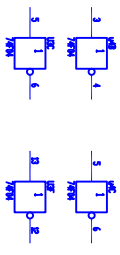
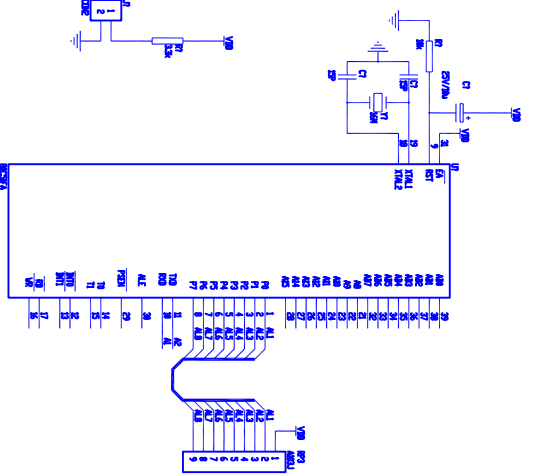
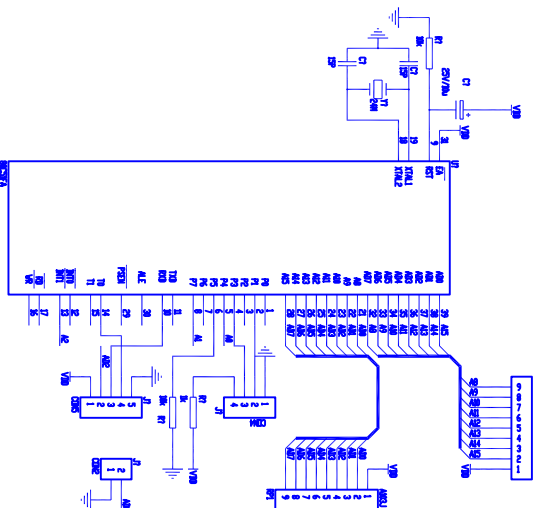
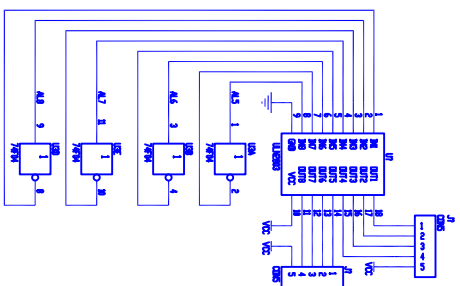
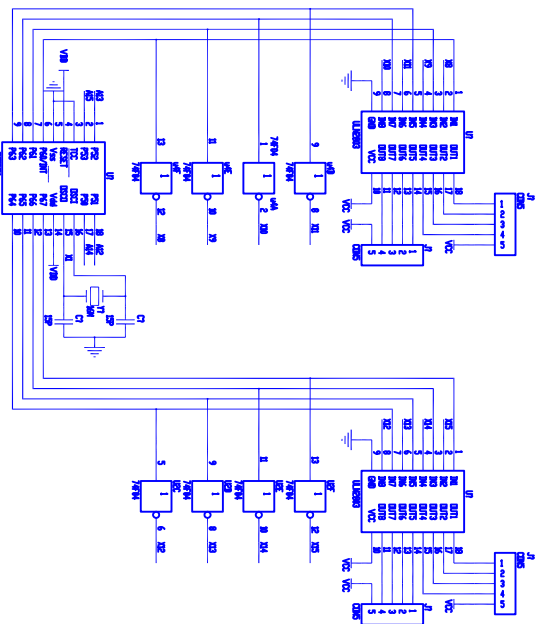
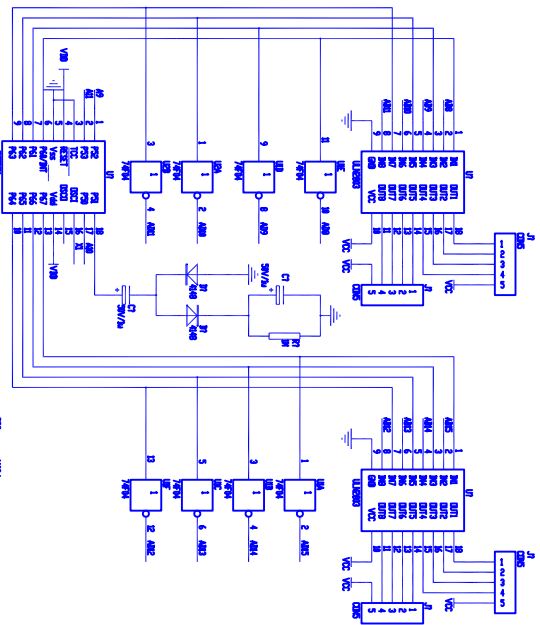
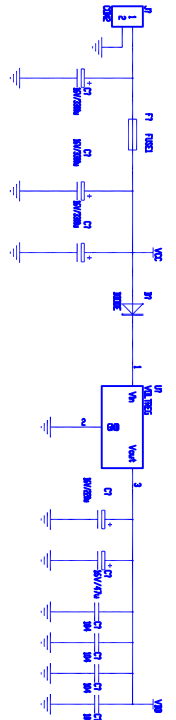
Number

Date

File

Sheet of

Revision



Symbol	Value	Quantity
74LS161	4-bit binary counter	1
74LS160	4-bit binary counter	1
74LS83	4-bit binary counter	4
74LS00	NAND gate	4
74LS04	Inverter	4
74LS08	AND gate	4
74LS09	OR gate	4
74LS10	NAND gate	4
74LS11	AND gate	4
74LS12	OR gate	4
74LS13	Inverter	4
74LS14	Inverter	4
74LS15	NAND gate	4
74LS16	AND gate	4
74LS17	OR gate	4
74LS18	NAND gate	4
74LS19	AND gate	4
74LS20	OR gate	4
74LS21	NAND gate	4
74LS22	AND gate	4
74LS23	OR gate	4
74LS24	NAND gate	4
74LS25	AND gate	4
74LS26	OR gate	4
74LS27	NAND gate	4
74LS28	AND gate	4
74LS29	OR gate	4
74LS30	NAND gate	4
74LS31	AND gate	4
74LS32	OR gate	4
74LS33	NAND gate	4
74LS34	AND gate	4
74LS35	OR gate	4
74LS36	NAND gate	4
74LS37	AND gate	4
74LS38	OR gate	4
74LS39	NAND gate	4
74LS40	AND gate	4
74LS41	OR gate	4
74LS42	NAND gate	4
74LS43	AND gate	4
74LS44	OR gate	4
74LS45	NAND gate	4
74LS46	AND gate	4
74LS47	OR gate	4
74LS48	NAND gate	4
74LS49	AND gate	4
74LS50	OR gate	4
74LS51	NAND gate	4
74LS52	AND gate	4
74LS53	OR gate	4
74LS54	NAND gate	4
74LS55	AND gate	4
74LS56	OR gate	4
74LS57	NAND gate	4
74LS58	AND gate	4
74LS59	OR gate	4
74LS60	NAND gate	4
74LS61	AND gate	4
74LS62	OR gate	4
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74LS65	OR gate	4
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74LS67	AND gate	4
74LS68	OR gate	4
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74LS79	AND gate	4
74LS80	OR gate	4
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74LS82	AND gate	4
74LS83	OR gate	4
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74LS85	AND gate	4
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74LS88	AND gate	4
74LS89	OR gate	4
74LS90	NAND gate	4
74LS91	AND gate	4
74LS92	OR gate	4
74LS93	NAND gate	4
74LS94	AND gate	4
74LS95	OR gate	4
74LS96	NAND gate	4
74LS97	AND gate	4
74LS98	OR gate	4
74LS99	NAND gate	4